Paper Title

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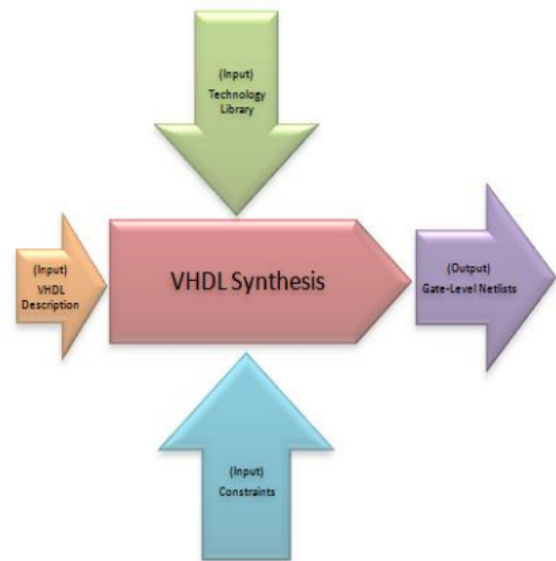

Figure 1. VHDL Synthesis Process

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TABLE I. Evolution of test-bed clusters

Label	CPU Type	Frequency	cpu#	core#	node#	RAM	HT?	TB?	CS?
A	Intel® E6550	2.00-2.33GHz	1	2	6	4GB	N	N	N
B	Opteron™ 2354	1.1-2.2GHz	2	4	16	16GB	N	N	N
C	Xeon™ E5-2640v2	1.1-2.0GHz	2	8	4	32GB	Y	Y	Y

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4. BIBLIOGRAPHY STYLES

Here are two sample references: [1], [2]

5. CONCLUSIONS AND FUTURE WORK

In this paper we have presented an extensible CPU power measurement framework that supports our own research but is also generally applicable to the computer engineering community in general for accurate computational power consumption measurements.

REFERENCES

[1] J. Gong and N. J. Navimipour, "An in-depth and systematic literature review on the blockchain-based approaches for cloud computing," *Cluster Computing*, pp. 1–18, 2021.

[2] C. V. B. Murthy, M. L. Shri, S. Kadry, and S. Lim, "Blockchain based cloud computing: Architecture and research challenges," *IEEE Access*, vol. 8, pp. 205 190–205 205, 2020.